

# Modbus Communications For Models PVI 14-36TL

**Revision B** 

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# 1. Yaskawa - Solectria Solar PVI 14-36TL Modbus

### 1.1 Introduction

This manual explains the Modbus protocol in use for Yaskawa - Solectria Solar's PVI 14-36TL inverters. This does not include all the Modbus protocol details. It is expected that the person(s) reading this manual have a clear understanding of Modbus protocol.

Each register/data point contains a raw 16-bit signed or unsigned number. The appropriate multiplier must be applied to each word to obtain the scaled representation.

#### 1.2 Abbreviations

| IP     | Internet Protocol          |
|--------|----------------------------|
| MB     | MODBUS                     |
| ТСР    | Transport Control Protocol |
| uint16 | 16bit unsigned integer     |
| uint32 | 32bit unsigned integer     |
| unit64 | 64bit unsigned integer     |
| int16  | 16bit signed integer       |
| int32  | 32bit signed integer       |

### **1.3 Modbus Information**

The Yaskawa - Solectria Solar PVI 14-36TL inverters Modbus register listing may contain some registers that are not supported for all inverter models. The inverter will respond with the following values if a feature is not supported for a specific inverter model.

Not Implemented for an int16 is 0x8000. Not Implemented for a uint16 is 0xFFFF. Not Implemented for an int32 is 0x80000000. Not Implemented for a uint32 is 0xFFFFFFFF. Not Implemented for a string is 0x00.

## 1.4 Data Encoding

The MODBUS specification is not explicit on how to encode numbers other than 16-bit integers. Differences do exist between one manufacturer's implementation and another's. 32 bit and 64 bit data for PVI 14-36TL inverters is encoded as described below.

#### 32-bit integer Value

Values are stored in big-endian (i.e. most significant byte first) order per the MODBUS specification and consist of two consecutive 16-bit registers.

| MODBUS Register | S Register 1 2 |      |     |    |  |
|-----------------|----------------|------|-----|----|--|
| byte            | 0              | 1    | 2   | 3  |  |
| bits            | 3124           | 2316 | 158 | 70 |  |

#### 64-bit integer Value

64-bit integers are stored using for registers in big-endian order and consists of 4 consecutive 16-bit registers.

| MODBUS Register |         | 1       |         | 2       |         | 3       | 4      |       |  |
|-----------------|---------|---------|---------|---------|---------|---------|--------|-------|--|
| byte            | 0       | 1       | 2       | 3       | 4       | 5       | 6      | 7     |  |
| bits            | 63 - 56 | 55 - 48 | 47 - 40 | 39 - 32 | 31 - 24 | 23 - 16 | 15 - 8 | 7 - 0 |  |

#### 1.5 RS485 Modbus

Modbus Mode: RTU Data Bits: 8 Parity: None Stop Bits: 1 Mode: Half-Duplex Baud Rate: 9600 or 19200 Modbus Id Range: 1-128 Modbus Cable Distance: 3000ft with no more than 300 ft between inverters Modbus Communications PVI 14-36TL (Rev B)

# 2 Modbus Register Tables

# 2.1 Inverter Input (Read Only) Registers

The MODBUS read function code for the PVI 14-36TL inverter Input (RO) registers is 0x04.

| Modbus<br>Register | Register<br>Size | Description  | Туре       | Unit | Multiplier | Туре       | RD/<br>WR | Supported<br>Inverter<br>Model     |
|--------------------|------------------|--|------------|------|------------|------------|-----------|------------------------------------|
| 0x0005             | 1                | Inverter Firmware Versions<br>The data format is 0xAABB where AA indicates DSP Firmware<br>version and BB indicates LCD Firmware version.  | uint16     | N/A  | 0.01       | uint16     | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0006             | 4                | Inverter Serial Number<br>The Serial Number is composed of 13 digits represented as the 13<br>Least Significant bytes. E.g. a read value 0X0001013011353001 is<br>equal to<br>Serial Number 1013011353001  | uint64     | N/A  | BCD        | uint64     | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x000A             | 10               | Inverter Model Information<br>Identifies the TL Inverter Model information;<br>I.e. PVI14TL-208, PVI20TL-480, PVI23TL-480, PVI28TL-480,<br>PVI36TL-480   | string(20) | N/A  | 1          | string(20) | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0016             | 2                | Total Lifetime Energy Produced to the Grid   | uint32     | kWh  | 1          | uint32     | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0018             | 1                | Accumulated Energy Production for the Day  | uint16     | kWh  | 0.1        | uint16     | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x001A             | 1                | Power Factor reading<br>E.g. when reading this register 0x0320 is representing<br>PF = 0x0320 *0.001 = 0.8<br>For PVI 14 - 20TL the reading is the actual Power Factor setting.<br>For PVI 23 -36TL this reading is a calculated reading based on<br>inverter measurement. | int16      | N/A  | 0.001      | int16      | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x001B             | 1                | Maximum Active AC Power during the Day   | uint16     | kW   | 0.1        | uint16     | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x001D             | 1                | Active AC Power Reading  | uint16     | kW   | 0.1        | uint16     | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |

| Modbus<br>Register | Register<br>Size | Description                       | Туре   | Unit | Multiplier | Туре   | RD/<br>WR | Supported<br>Inverter<br>Model     |
|--------------------|------------------|-----------------------------------|--------|------|------------|--------|-----------|------------------------------------|
| 0x001E             | 1                | Apparent AC Power Reading         | uint16 | kVA  | 0.1        | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x001F             | 1                | Grid Voltage Uab                  | uint16 | v    | 0.1        | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0020             | 1                | Grid Voltage Ubc                  | uint16 | v    | 0.1        | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0021             | 1                | Grid Voltage Uca                  | uint16 | v    | 0.1        | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0022             | 1                | Grid A Phase Current              | uint16 | A    | 0.1        | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0023             | 1                | Grid B Phase Current              | uint16 | A    | 0.1        | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0024             | 1                | Grid C Phase Current              | uint16 | A    | 0.1        | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0025             | 1                | DC Voltage, MPPT Zone 1           | uint16 | v    | 0.1        | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0026             | 1                | DC Current, MPPT Zone 1           | uint16 | А    | 0.1        | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0027             | 1                | DC2 Voltage, MPPT Zone 2          | uint16 | v    | 0.1        | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0028             | 1                | DC2 Current, MPPT Zone 2          | uint16 | A    | 0.1        | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x002B             | 1                | Grid Frequency                    | uint16 | Hz   | 0.1        | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x002C             | 1                | Power Module heatsink temperature | int16  | с    | 0.1        | int16  | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |

| Modbus<br>Register | Register<br>Size | Description   | Туре   | Unit | Multiplier | Туре   | RD/<br>WR | Supported<br>Inverter<br>Model     |
|--------------------|------------------|---|--------|------|------------|--------|-----------|------------------------------------|
| 0x002D             | 1                | Internal Inverter Temperature   | int16  | С    | 0.1        | int16  | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x002F             | 1                | Inverter Operation State information<br>See the "Inverter States" section for detailed description  | uint16 | N/A  | 1          | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0030             | 4                | Error Timestamp<br>(yyyy-mm-dd-hh-mm-ss-NULL)<br>eg.0x2012071615181000 = 2012-7-16 15:18:10   | uint64 | N/A  | BCD        | uint64 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0034             | 1                | Permanent Fault (PFault) Alarm Register<br>Severity - High<br>Requires site visit to check the cause. The inverter will stay off until<br>both DC and AC power are cycled.  | uint16 | N/A  | 1          | Uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0035             | 1                | Warning Alarm Register<br>Severity - Low<br>The inverter discovered a small anomaly, but will continue to<br>generate power.  | uint16 | N/A  | 1          | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0036             | 1                | FaultO Alarm Register<br>Severity - Medium<br>The inverter has discovered a failure such as grid or PV out of<br>range conditions. The inverter will resume power generation when<br>the condition clears.  | uint16 | N/A  | 1          | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0037             | 1                | Fault1 Alarm Register<br>Severity - Medium<br>The inverter has discovered a failure such as grid or PV out of<br>range conditions. The inverter will resume power generation when<br>the condition clears.  | uint16 | N/A  | 1          | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |
| 0x0038             | 1                | Fault2 Alarm Register<br>Severity - Medium<br>The inverter has discovered a failure such as grid or PV out of<br>range conditions. The inverter will resume power generation when<br>the condition clears.<br>Exception: Bits 15 Arc Fault in models PVI 14TL and PVI 20TL are<br>high severity and stay on permanently like other PFaults. | uint16 | N/A  | 1          | uint16 | RD        | 14TL, 20TL,<br>23TL, 28TL,<br>36TL |

| Modbus<br>Register | Register<br>Size | Description   | Туре   | Unit | Multiplier | Туре   | RD/<br>WR | Supported<br>Inverter<br>Model |
|--------------------|------------------|---|--------|------|------------|--------|-----------|--------------------------------|
| 0x0039             | 1                | Fault3 Alarm Register (PVI 23-36TL only)<br>Severity - Medium<br>The inverter has discovered a failure such as grid or PV out of<br>range conditions. The inverter will resume power generation when<br>the condition clears.<br>Exception: Arc Fault condition indicated by bit 15 will shut the<br>inverter production down until the Arc Fault condition and both<br>DC and AC power are recycled. | uint16 | N/A  | 1          | uint16 | RD        | 23TL, 28TL,<br>36TL            |
| 0x003A             | 1                | Fault4 Alarm Register (PVI 23-36TL only)<br>Severity - Medium<br>The inverter has discovered a failure such as grid or PV out of<br>range conditions. The inverter will resume power generation when<br>the condition clears.<br>Exception: Arc Fault condition indicated by bit 2 will shut inverter<br>production down until the Arc Fault condition and both DC and AC<br>power are recycled.      | uint16 | N/A  | 1          | uint16 | RD        | 23TL, 28TL,<br>36TL            |

### 2.2 Inverter States

The PVI 14-36TL inverters have 5 operational states built into the inverters as described in the table below. The inverter operational state can be read from either the front panel LEDs or by reading Modbus register 0x002F.

|                     |        |                   |  |       | LED S | tate |       |
|---------------------|--------|-------------------|--|-------|-------|------|-------|
| Register<br>Address | Data   | Inverter<br>State | Description  | Power | Run   | Grid | Fault |
|                     | 0x8000 | Fault             | Inverter is in Fault State and is not producing power.<br>Site visit may be required.  | On    | Off   | On   | Blink |
|                     | 0x4000 | Check             | Inverter is checking the input and output status. If the<br>input and output conditions are met and no failure is<br>detected, the Inverter will connect to the grid and<br>begin generating power and transfer to Running state | On    | Off   | On   | Off   |
| 0x002F              | 0x2000 | Standby           | Inverter is not generating power due to either low PV<br>power or a manual shutdown. If the inverter is in<br>standby State due to low PV power it will change to<br>Check State when enough PV power is available.              | On    | Off   | On   | Off   |
|                     | 0x1000 | Running           | Inverter is running normally and generating power.   | On    | On    | On   | Off   |
|                     | 0x0800 | Derating          | On   | Blink | On    | Off  |       |

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# 2.3 Inverter Holding (Control RD/WR) Registers

The Modbus read function code for the PVI 14-36TL inverter holding (RD/WR) registers is 0x03. The Modbus write function code is 0x06 for a single register write and 0x10 for multiple register writes.

Note! Please do not write to any of the reserved or unlisted register addresses. Doing so could damage the inverter and void the warranty.

| Modb.<br>Reg. | Reg<br>Size | Description  | Name     | Unit | Туре   | Multiplier | Default<br>Value | Min<br>Value    | Max<br>Value   | Support.<br>Inverter<br>Model            |
|---------------|-------------|--|----------|------|--------|------------|------------------|-----------------|----------------|--|
| 0x1000        | 1           | Inverter Power On and Power Off command<br>OxAAAA turns the unit power production On and<br>OX5555 turns the unit power production Off<br>The Inverter will automatically turn back on whenever DC<br>power is cycled. E.g. Inverter will turn back on in the<br>morning when the sun comes up.                                | On/Off   | N/A  | uint16 | 1          | 0xAAAA<br>(On)   | 0x5555<br>(OFF) | 0xAAAA<br>(ON) | 14TL,<br>20TL,<br>23TL,<br>28TL,<br>36TL |
| 0x1001        | 1           | Temporary Active Power setting (curtailment)<br>This setting will be reset overnight or when the inverter is<br>power cycled.<br>Curtailment mode must be enabled by writing 1 to register<br>0x1046.<br>Range [0.0%,100.%] of nameplate kW rating,<br>E.g. 70.7%, then TmpPSet = 0x02c3<br>For PVI 36TL: 36kW * 70.7% = 25 kW | TmpPSet  | %    | uint16 | 0.10       | 1000             | 0               | 1000           | 14TL,<br>20TL,<br>23TL,<br>28TL,<br>36TL |
| 0x1002        | 1           | Temporary Power Factor setting<br>This setting will be reset to its default value overnight or<br>when the inverter is power cycled.<br>Power Factor mode must be enabled by writing 1 to<br>register 0x1047 for the 23TL, 28TL and 36TL inverters only.   | TmpPFSet | N/A  | int16  | 0.001      | 1000             | -1000,<br>-800  | 800,<br>1000   | 14TL,<br>23TL,<br>28TL,<br>36TL          |
| 0002          |             | Temporary Power Factor setting takes priority over the<br>Permanent Power Setting in register 0x1014<br>E.g. 0.931 then TmpPFSet = 0X03A3<br>-0.931 then TmpPFSet = 0xFC5D   | mpriset  | IN/A |        | 0.001      | 1000             | -1000,<br>-900  | 900,<br>1000   | 20TL                                     |

| Modb.<br>Reg. |   | Description   | Name      | Unit | Туре   | Multiplier | Default<br>Value | Min<br>Value | Max<br>Value | Support.<br>Inverter<br>Model            |
|---------------|---|---|-----------|------|--------|------------|------------------|--------------|--------------|--|
| 0x1003        | 1 | Temporary Reactive Power setting<br>This setting will be reset overnight or when the inverter is<br>power cycled.<br>The inverter can produce Reactive Power up to a maximum<br>of 60% of its nameplate rating. Range = 0% - 60%.<br>Reactive Power mode must be enabled by writing 1 to<br>register 0x1047 for the PVI 23/28TL/36TL inverters only.<br>E.g. 50% Var based on the nameplate rating,<br>then TmpQSet = 0x01F4<br>For PVI 36TL: 36kW * 50% = 18kVar | TmpQSet   | %    | uint16 | 0.10       | 0                | -600         | 600          | 14TL,<br>20TL,<br>23TL,<br>28TL,<br>36TL |
| 0x1004        | 4 | System time setting<br>Format: yyyy-mm-dd-hh-mm-ss-NUL,<br>e.g. 0x2012071615181000 = 2012-7-16 15:18:10   | TimeSet   | N/A  | uint64 | BCD        | N/A              | N/A          | N/A          | 14TL,<br>20TL,<br>23TL,<br>28TL,<br>36TL |
| 0x100B        | 1 | High Grid Frequency Trip Time 1<br>Defines the amount of time the inverter takes to trip when<br>the High Grid Frequency Trip Limit 1 is exceeded.  | TFMaxTrip | sec  | uint16 | 0.01       | 300              | 0            | 600          | 14TL,<br>20TL,<br>23TL,<br>28TL,<br>36TL |
|               |   | High Grid Voltage Trip Limit 1  |           |      |        |            | 2280             | 2240         | 2490         | 14TL                                     |
| 0x100C        | 1 | Defines the high grid voltage limit of when the inverter trips.   | VMax      | V    | uint16 | 0.1        | 5280             | 4800         | 5520         | 20TL,<br>23TL,<br>28TL,<br>36TL          |
| 0x100D        | 1 | High Grid Voltage Trip Time 1<br>Controls the time delay it takes the inverter to trip when<br>the High Grid Voltage Trip Limit 1is exceeded.   | TVmaxTrip | sec  | uint16 | 0.01       | 1                | 5            | 600          | 14TL,<br>20TL,<br>23TL,<br>28TL,<br>36TL |
|               |   |   |           |      |        |            | 1830             | 1040         | 1870         | 14TL                                     |
| 0x100E        | 1 | Low Grid Voltage Trip Limit 1<br>Defines the low grid voltage limit of when the inverter trips.   | VMin      | V    | uint16 | 0.1        | 4224             | 2400         | 4800         | 20TL,<br>23TL,<br>28TL,<br>36TL          |

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| Modb.<br>Reg. |   | Description  | Name      | Unit | Туре   | Multiplier | Default<br>Value | Min<br>Value | Max<br>Value  | Support.<br>Inverter<br>Model            |
|---------------|---|--|-----------|------|--------|------------|------------------|--------------|---------------|--|
| 0x100F        | 1 | Low Grid Voltage Trip Time 1<br>Defines the time it takes the inverter to trip when the Low<br>Grid Voltage Trip Limit 1 is exceeded.  | TVminTrip | sec  | uint16 | 0.01       | 2                | 5            | 600           | 14TL,<br>20TL,<br>23TL,<br>28TL,<br>36TL |
| 0x1010        | 1 | High Grid Frequency Trip Limit 1<br>Defines the high grid frequency limit of when the inverter<br>trips.   | FMax      | Hz   | uint16 | 0.01       | 6050             | 6000         | 6500          | 14TL,<br>20TL,<br>23TL,<br>28TL,<br>36TL |
| 0x1011        | 1 | Low Grid Frequency Trip Limit 1<br>Defines the low grid frequency limit of when the inverter<br>trips.   | FMin      | Hz   | uint16 | 0.01       | 5930             | 4500         | 6000          | 14TL,<br>20TL,<br>23TL,<br>28TL,<br>36TL |
| 0x1012        | 1 | Low Grid Frequency Trip Time 1<br>Defines the time it takes the inverter to trip when the Low<br>Grid Frequency Trip Limit 1 is exceeded.  | TFMinTrip | sec  | uint16 | 0.01       | 16               | 5            | 6000          | 14TL,<br>20TL,<br>23TL,<br>28TL,<br>36TL |
| 0x1013        | 1 | Permanent Active Power Setting (curtailment)<br>This Active Power setting will be retained overnight and<br>when the inverter is power cycled.<br>Range [0.0%, 100.0%],<br>E.g. 70.7%, then P_EE = 0x02c3<br>Permanent Active Power Setting has to be enabled by<br>writing 1 to register 0x1046 for the 23TL - 36TL inverters<br>only.  | P_EE      | %    | uint16 | 0.10       | 1000             | 0            | 1000          | 14TL,<br>20TL,<br>23TL,<br>28TL,<br>36TL |
|               |   | Permanent Power Factor Setting<br>This setting will be retained overnight and when the   |           |      |        |            |                  | 1000<br>9000 | -9000<br>1000 | 20TL                                     |
| 0x1014        | 1 | inverter is power cycled.<br>Writing to the TmpPFSet register (0x1002) will overwrite<br>this setting. The last one written to is in effect.<br>E.g. 0.931 then PrmPFSet = 0X03A3<br>-0.931 then PrmPFSet = 0xFC5D<br>Power Factor mode must be enabled by writing 1 to<br>register 0x1047 for the 23TL, 28TL and 36TL inverters only.<br>The Temporary Power Factor setting in register 0x1002<br>takes precedence over the Permanent Power Setting | PrmPFSet  | N/A  | int16  | 0.001      | 1000             | 1000<br>8000 | -8000<br>1000 | 14TL,<br>23TL,<br>28TL,<br>36TL          |

| Modb.<br>Reg. | Reg<br>Size | Description  | Name        | Unit   | Туре   | Multiplier | Default<br>Value | Min<br>Value | Max<br>Value | Support.<br>Inverter<br>Model   |
|---------------|-------------|--|-------------|--------|--------|------------|------------------|--------------|--------------|---------------------------------|
| 0x1015        | 1           | Startup Delay Time<br>The delay time the inverter waits before starting power<br>generation and connecting to the grid, after the DC and grid<br>connections conditions are met.       | StartDelay  | sec    | uint16 | 1          | 60               | 0            | 600          | 23TL,<br>28TL,<br>36TL          |
| 0x101D        | 1           | Ramp Rate Setting<br>Defines the Soft-Start power-on ramp rate of when the<br>inverter reconnects to the grid.<br>Note: Other Ramp Rates are not Controllable using Modbus<br>control. | SoftPStep   | kW/sec | uint16 | 0.01       | 1.44             | 0.01         | 2.88         | 14TL,<br>23TL,<br>28TL,<br>36TL |
| 0x101E        | 1           | The 2 <sup>nd</sup> level high grid voltage limit  | VMaxII      | v      | uint16 | 0.1        | 5760             | 4800         | 6240         | 23TL,<br>28TL,<br>36TL          |
|               |             |  |             |        |        |            | 2496             | 2288         | 4850         | 14TL                            |
| 0x101F        | 1           | Trip time when the VMAXII is exceeded  | TVmaxTripII | sec    | uint16 | 0.01       | 16               | 5            | 60000        | 14TL,<br>23TL,<br>28TL,<br>36TL |
| 0x1020        | 1           | Trip point for under voltage at 2 <sup>nd</sup> level  | VMinII      | v      | uint16 | 0.1        | 2400             | 2400         | 4800         | 23TL,<br>28TL,<br>36TL          |
|               |             |  |             |        |        |            | 1040             | 1000         | 1830         | 14TL                            |
| 0x1021        | 1           | Trip time for under voltage at 2 <sup>nd</sup> level   | TVminTripII | sec    | uint16 | 0.01       | 16               | 5            | 60000        | 14TL,<br>23TL,<br>28TL,<br>36TL |
| 0x1022        | 1           | Trip point for over frequency at 2 <sup>nd</sup> level   | FMaxII      | Hz     | uint16 | 0.01       | 6400             | 5000         | 6500         | 36TL                            |
| 0x1023        | 1           | Trip time for over frequency at 2 <sup>nd</sup> level  | TFMaxTripII | sec    | uint16 | 0.01       | 5                | 5            | 60000        | 36TL                            |
| 0x1024        | 1           | Trip point for under frequency at 2 <sup>nd</sup> level  | FMinII      | Hz     | uint16 | 0.01       | 5900             | 4500         | 6000         | 36TL                            |
| 0x1025        | 1           | Trip time for under frequency at 2 <sup>nd</sup> level   | TFMinTripII | sec    | uint16 | 0.01       | 5                | 5            | 60000        | 36TL                            |
| 0x1026        | 1           | Recovery voltage point after over voltage trip   | VMaxRcov    | v      | uint16 | 0.1        | 5180             | 2000         | 5330         | 23TL,<br>28TL,<br>36TL          |
|               |             |  |             |        |        |            | 2250             | 2280         | 2496         | 14TL                            |
| 0x1027        | 1           | Reconnection voltage point after under voltage trip  | VMinRcov    | V      | uint16 | 0.1        | 4324             | 0            | 4800         | 23TL,<br>28TL,<br>36TL          |

| Modb.<br>Reg. | Reg<br>Size | Description  | Name            | Unit | Туре   | Multiplier | Default<br>Value | Min<br>Value | Max<br>Value | Support.<br>Inverter<br>Model   |
|---------------|-------------|--|-----------------|------|--------|------------|------------------|--------------|--------------|---------------------------------|
|               |             |  |                 |      |        |            | 1860             | 1040         | 2080         | 14TL                            |
| 0x1028        | 1           | Reconnection delay time after grid voltage failure   | VRcovT          | sec  | uint16 | 0.01       | 300              | 0            | 600          | 14TL,<br>23TL,<br>28TL,<br>36TL |
| 0x1029        | 1           | Reconnection frequency point after over frequency trip   | FMaxRcov        | Hz   | uint16 | 0.01       | 6040             | 5000         | 6500         | 14TL,<br>23TL,<br>28TL,<br>36TL |
| 0x102A        | 1           | Reconnection frequency point after under frequency trip  | FMinRcov        | Hz   | uint16 | 0.01       | 5940             | 4500         | 6000         | 14TL,<br>23TL,<br>28TL,<br>36TL |
| 0x102B        | 1           | Reconnection delay time after grid frequency failure   | FRcovT          | sec  | uint16 | 0.01       | 300              | 0            | 600          | 14TL,<br>23TL,<br>28TL,<br>36TL |
| 0x102C        | 1           | Frequency entry point for the Frequency - Watt<br>functionality<br>Note: The inverter needs to be configured for Rule 21<br>mode via the front panel LCD for this feature to work.   | FDeratStart     | Hz   | uint16 | 0.01       | 6050             | 5000         | 6500         | 36TL                            |
| 0x102D        | 1           | Frequency exit point for the Frequency-Watt functionality<br>Note: The inverter needs to be configured for Rule 21<br>mode via the front panel LCD for this feature to work.   | FDeratStop      | Hz   | uint16 | 0.01       | 6400             | 6000         | 6500         | 36TL                            |
| 0x102E        | 1           | Enable or disable Low Voltage Ride Through (LVRT)<br>1 = No reactive Power output during LVRT<br>2= Reactive Power output during LVRT<br>LVRT is disabled when IEEE 1547 grid standard enabled. See<br>the installation manual for more information. | LvrtEn          | N/A  | uint16 | 1          | 0                | 0            | 2            | 36TL                            |
| 0x102F        | 1           | Low Voltage Ride Through (LVRT)  | VLvrtStart      | %    | uint16 | 0.1        | 880              | 700          | 900          | 36TL                            |
| 0x1033        | 1           | Power Factor Control Mode (PFP) (Mode 4):<br>The first % value for the PF versus Power curve.<br>See Smart Grid section below for more details.  | PF_PCurveActPw1 | %    | uint16 | 0.1        | 50               | 0.1          | 100          | 36TL                            |
| 0x1034        | 1           | Power Factor Control Mode (PFP) (Mode 4):<br>The first PF value for the PF versus Power curve.<br>See Smart Grid section below for more details.   | PF_PCurvePF1    | N/A  | uint16 | 0.001      | 1000             | -800         | 800          | 36TL                            |
| 0x1035        | 1           | Power Factor Control Mode (PFP) (Mode 4):<br>The second % value for the PF versus Power curve.<br>See Smart Grid section below for more details.   | PF_PCurveActPw2 | %    | uint16 | 0.1        | 1000             | 0            | 1000         | 36TL                            |

| Modb.<br>Reg. |   | Description   | Name              | Unit | Туре   | Multiplier | Default<br>Value | Min<br>Value | Max<br>Value | Support.<br>Inverter<br>Model |
|---------------|---|---|-------------------|------|--------|------------|------------------|--------------|--------------|-------------------------------|
| 0x1036        | 1 | Power Factor Control Mode (PFP) (Mode 4):<br>The second PF value for the PF versus Power curve.<br>See Smart Grid section below for more details.   | PF_PCurvePF2      | N/A  | uint16 | 0.001      | -900             | -800         | 800          | 36TL                          |
| 0x1037        | 1 | Power Factor Control Mode (PFP) (Mode 4):<br>The PF versus Power curve turn on grid voltage.<br>The curve is active when the grid voltage is greater than this<br>voltage.<br>See Smart Grid section below for more details.  | F_PCurveLockInV   | V    | uint16 | 0.1        | 4800             | 4800         | 5280         | 36TL                          |
| 0x1038        | 1 | Power Factor Control Mode (PFP) (Mode 4):<br>The PF versus Power curve turn off grid voltage.<br>The curve is inactive when the grid voltage is lower than<br>this voltage.<br>See Smart Grid section below for more details. | PF_PCurveLockOutV | V    | uint16 | 0.1        | 4320             | 4224         | 4800         | 36TL                          |
| 0x1039        | 1 | Volt-VAr Mode:<br>Parameters for the Volt-VAr curve<br>See the Volt-VAr section for more details.   | Q_UCurveVolt1s    | V    | uint16 | 0.1        | 5184             | 4800         | 5280         | 36TL                          |
| 0x103A        | 1 | Volt-VAr Mode:<br>[Parameters for the Volt-VAr curve<br>See the Volt-VAr section for more details.  | Q_UCurveReactPw1s | %    | uint16 | 0.1        | 0                | -600         | 600          | 36TL                          |
| 0x103B        | 1 | Volt-VAr Mode:<br>Parameters for the Volt-VAr curve<br>See the Volt-VAr section for more details  | Q_UCurveVolt2s    | V    | uint16 | 0.1        | 5280             | 4800         | 5280         | 36TL                          |
| 0x103C        | 1 | Volt-VAr Mode:<br>Parameters for the Volt-VAr curve<br>See the Volt-VAr section for more details  | Q_UCurveReactPw2s | %    | uint16 | 0.1        | -500             | -600         | 600          | 36TL                          |
| 0x103D        | 1 | Volt-VAr Mode:<br>Parameters for the Volt-VAr curve<br>See the Volt-VAr section for more details  | Q_UCurveVolt1i    | V    | uint16 | 0.1        | 441.6            | 422.4        | 480          | 36TL                          |
| 0x103E        | 1 | Volt-VAr Mode:<br>Parameters for the Volt-VAr curve<br>See the Volt-VAr section for more details  | Q_UCurveReactPw1i | %    | uint16 | 0.1        | 0                | -600         | 600          | 36TL                          |
| 0x103F        | 1 | Volt-VAr Mode:<br>Parameters for the Volt-VAr curve<br>See the Volt-VAr section for more details  | Q_UCurveVolt2i    | V    | uint16 | 0.1        | 4320             | 4224         | 4800         | 36TL                          |
| 0x1040        | 1 | Volt-VAr Mode:<br>Parameters for the Volt-VAr curve<br>See the Volt-VAr section for more details  | Q_UCurveReactPw2i | %    | uint16 | 0.1        | 500              | -600         | 600          | 36TL                          |

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| Modb.<br>Reg. | Reg<br>Size | Description   | Name  | Unit | Туре   | Multiplier | Default<br>Value | Min<br>Value | Max<br>Value | Support.<br>Inverter<br>Model |
|---------------|-------------|---|---|------|--------|------------|------------------|--------------|--------------|-------------------------------|
| 0x1041        | 1           | Volt-VAr mode:<br>If the active power is greater than this number, Volt-VAr<br>mode is activated.   | Q_UCurveLockInP                             | %    | uint16 | 0.1        | 200              | 50           | 1000         | 36TL                          |
| 0x1042        | 1           | Volt-VAr mode:<br>If the active power is lower than this number, Volt-VAr<br>mode is deactivated.   | Q_UCurveLockOutP                            | %    | uint16 | 0.1        | 50               | 50           | 1000         | 36TL                          |
| 0x1043        | 1           | Frequency-Watt:<br>Over frequency derating enable/disable<br>1 = Reserved<br>2 = Rule 21 curve  | FreqDeratOption                             | N/A  | uint16 | 1          | 0                | 0            | 2            | 36TL                          |
| 0x1044        | 1           | Trip point of over-frequency power derating   | FDeratStart                                 | Hz   | uint16 | 0.01       | 6050             | 5000         | 6500         | 36TL                          |
| 0x1045        | 1           | Reserved  |   |      |        |            |                  |              |              |                               |
| 0x1046        | 1           | Enable or Disable temporary Power or Power Factor control<br>0 = Disable<br>1 = Enable  | RemoteActivePwDisp<br>atchModeOption        | N/A  | uint16 | N/A        | 0                | 0            | 1            | 23TL,<br>28TL,<br>36TL        |
| 0x1047        | 1           | This register is used to select the inverter Reactive Power<br>mode. This setting is a permanent setting that is maintained<br>through power cycling events.<br>0: Disable Reactive Power Control.<br>1: Enable Temporary Power Factor or Temporary Reactive<br>Power control<br>2: Enable Permanent Reactive Power Control.<br>3: Enable Permanent Power Factor Control<br>4: Enable Power Factor Control Mode<br>5: Enable Volt-VAr mode:<br>See the Reactive Power Control section for more details. | Reactive Power<br>generation<br>Mode Select | N/A  | uint16 | N/A        | 0                | 0            | 5            | 23TL,<br>28TL,<br>36TL        |

# **3** Smart Grid Features

#### 3.1 VAR and PF Control Operation

Yaskawa - Solectria Solar PVI 23-36TL inverters are capable of providing Volt-VAr control to support voltage regulations at the local grid. The inverters can produce either inductive or capacitive VAr, up to 60% of its kVA nameplate rating. The amount of VAR produced can be programmed at inverter via its LCD display interface locally or remotely through Modbus communications.

The inverters can provide VAr by two means. Either by enabling VAr control mode or PF-control mode

- In VAr-control mode, the inverters will provide a fixed amount of VAr regardless of the amount of Real Power available from PV. The amount of VAr can be programmed to be dependent on Grid grid voltage.
- In PF-control mode, the inverters will change the amount of VAr in accordance with the amount of Real Power available from the PV array to maintain the programmed PF value

The Yaskawa - Solectria Solar PVI 23-36TL inverters are capable of running in 4 different reactive power modes. One of the Reactive Power modes must be selected before attempting to control any of the Reactive Power control functionality.

#### 3.1.1 Disable Reactive Power Control (Mode 0)

The inverter will not produce any reactive power in Disable Reactive Power Control Mode (0).

#### **Register Write Procedure:**

1. Write 0 to register 0x1047

#### 3.1.2 Fixed Temporary Power Factor or Fixed Temporary Reactive Power Control (Mode 1)

When the inverter is in Temporary Power Factor or Temporary Reactive Power mode (Mode 1), the inverter will produce a fixed amount of Reactive Power or Power Factor according to the settings in registers TmpPFSet (0x1002) or TmpQSet (0x1003). The setting for these registers is temporary and will be reset back to default value overnight or when the inverter is power cycled.

#### **Register Write Procedure:**

- 1. Write 1 to register 0x1046
- 2. Write 1 to register 0x1047
- 3. Write the desired value to register 0x1002 or 0x1003

#### 3.1.3 Permanent Fixed Reactive Power Control (Q\_EE) (Mode 2)

The reactive power control can only be configured using the LCD and the front panel keyboard. Please see the installation manual for more information.

#### 3.1.4 Permanent Fixed Power Factor Control (PF\_EE) (Mode 3)

In the Permanent Fixed Power Factor mode (Mode 3), the inverter will produce a fixed amount of Reactive Power according to the Permanent Power Factor register (0x1014, PrmPFSet) setting. The setting for this register is permanent and will not reset back to default value overnight or when the inverter is power cycled.

#### **Register Write Procedure:**

- 1. Write 0x5555 to register 0x1000 to turn the inverter Off.
- 2. Write 3 to register 0x1047
- 3. Write the desired value to register 0x1014
- 4. Write 0xAAAA to register 0x1000 to turn the inverter On.

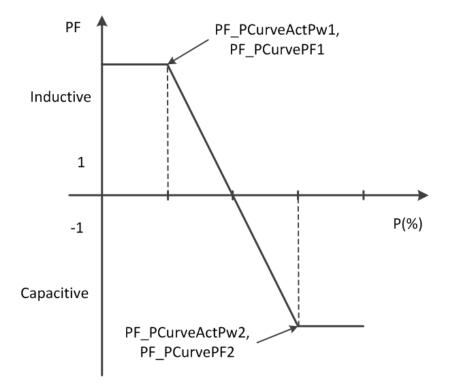
#### 3.1.5 Power Factor Control Mode (PFP) (Mode 4)

The PVI 23-36TL inverters can also provide VAr by programming the Power Factor.

- PF can be a fixed value in "PF mode". PF will be accurate up to +/- 0.01 of programmed value at load higher than 20% of its rating.
- PF can be programmed to vary with load percentage in "PFP mode". A preset curve is implemented in the inverter.
  - Unity PF from 0% to 50% load.
  - PF changes linearly from unity to a programmed PF with respect to load from 50% to 100%.

When PF is different than 1, the inverter will automatically reduce max Real Power to keep its output current below rated value.

The inverter will produce reactive power according to the PF curve to active power. The curve is defined using registers 0x1033 - 0x1038" as shown in the figure below.

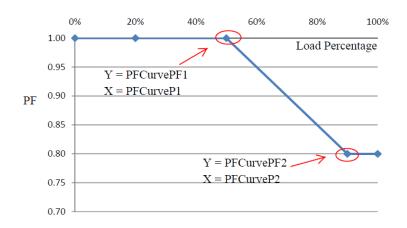


PVI 14-36TL Power Factor Terminology:

- Positive PF means exporting current lagging grid voltage (providing inductive VAR)
- Negative PF means exporting current leading grid voltage (providing capacitive VAR)

#### Example:

The curve below shows that the inverter will produce capacitive power PFCurvePF1(PF=-0.8) if the active power is greater than PFCurveP2(80%P). If the active power is lower than PFCurveP1(50%P), PF=1. When active power (P) increases from 50% to 80%, PF decreases linearly from 1 to -0.8.



#### 3.1.6 Volt-VAr Mode (QU) (Mode 5):

The inverters can provide VAr up to 60% of its kW/kVA nameplate rating with 0.1% resolution. For example, a 36kW inverter can provide up to 21.6kVAr of either inductive or capacitive Reactive Power. When the inverter is programmed to provide VAr, the amount of Real Power exported to the Grid is reduced in order to keep the output current below its maximum rating. The relationship of VAr, W, and VA is

# $VA = \sqrt{VAR^2 + W^2}$

For example with a 36kW inverter, at 21.6kVAr setting, inverter will provide maximum of 28.8kW of Real Power according to the calculation below.

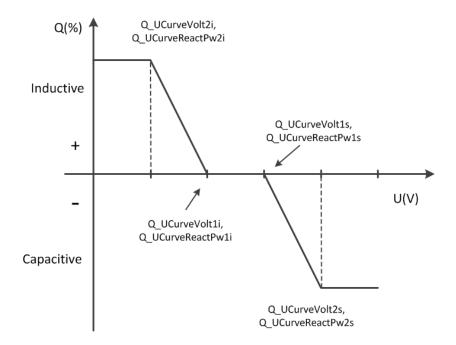
$$36 = \sqrt{21.6^2 + 28.8^2}$$

With VAr control, the inverters will maintain the programmed Reactive Power VAr while Real Power changes with sunlight condition.

PVI 14-36TL VAr Terminology:

- Inductive Reactive Power exporting to grid is denoted as Positive VAR
- Capacitive Reactive Power exporting to grid is denoted as Negative VAR

The amount of VAr can be programmed to linearly vary with grid voltage measured at the AC terminals of the inverter. The VAR curve and programmable points are shown in the Figure below. The inverter can be programmed either by using the LCD interface or Modbus communications using registers 0x1039 - 0x1042.



#### **Calculating the Volt-VAr Output:**

The Reactive Power output in Volt-VAr mode can be calculated in the following manner. Reactive Power (VAr) output = TmpQSet/1000 \* Inverter Rated Power. Note that VAr is limited to 60% of the inverter nameplate power rating.

#### **Register Write Procedure:**

- 1. Write 0x5555 to register 0x1000 to turn the inverter Off.
- 2. Write 5 to register 0x1047
- 3. Write the desired Volt-VAr curve values to register 0x1039-0x1042.
- 4. Write 0xAAAA to register 0x1000 to turn the inverter On.

# 4 Inverter Alarms and Alarm Descriptions

This section describes the PVI 14-36TL alarm register functionalities.

The alarms can be grouped into 3 categories, Warning, Fault and Permanent Fault (PFault), as described in the table below.

| Alarm    |  |          | Site Visit | Front       |
|----------|--|----------|------------|-------------|
| Category | Description  | Severity | Required   | Panel LED   |
| Warning  | A low severity level warning has been detected at the Inverter.    | Low      | No         | Slow Blink  |
| warning  | The Inverter can continue to operate and generate power.           | LOW      | NO         | SIOW DIIIIK |
|          | A medium severity level fault has been detected at the             |          |            |             |
| Fault    | Inverter. The Inverter will shut down and stop generating          | Medium   | No         | Fast Blink  |
|          | power until the fault is cleared.                                  |          |            |             |
|          | A high severity fault level has been detected at the Inverter.     |          |            | Steady      |
| PFault   | The Inverter will shut down and will not restart by itself even if | High     | Yes        | LED On      |
|          | the fault is cleared.  |          |            | LED ON      |

Please note alarm mapping differences between Inverter models as indicated by the tables below.

### 4.1 Permanent Fault Register

Any Permanent Faults (PFaults) are considered serious and the alarm will stay on permanently. The inverter will not recover from this fault by itself. A site visit is required to perform thorough site and inverter inspection to determine if it is safe to turn the inverter back on. A site visit is required to clear any PFault alarm and is done by cycling both DC and AC power off and back on again.

| Register |          |       | Alarm/Protection/                  |                     |   | L     | ED Read | lout |       |
|----------|----------|-------|------------------------------------|---------------------|---|-------|---------|------|-------|
| Address  | Name/    | Bits  | Fault                              | LCD Display Readout | Description                               | Power | Run     | Grid | Fault |
|          |          | Bit15 | 15V of control board low fault     | Fault0160           | Protect0620 occurs >3 times in 5 minutes. | On    | Off     | On   | On    |
|          |          | Bit14 | Inverter open-loop self-test fault | Fault0150           | Protect230 occurs >3 times in 5 minutes.  | On    | Off     | On   | On    |
|          |          | Bit13 | Internal hardware fault            | Fault0140           | Protect0210 occurs >3 times in 5 minutes. | On    | Off     | On   | On    |
|          | PFault H | Bit12 | Power module fault                 | Fault0010           | Protect0120 occurs >3 times in 5 minutes. | On    | Off     | On   | On    |
|          | Prault H | Bit11 | Reserved                           |                     |   |       |         |      |       |
|          |          | Bit10 | Reserved                           |                     |   |       |         |      |       |
|          |          | Bit9  | Reserved                           |                     |   |       |         |      |       |
| 0x0034   |          | Bit8  | Hardware over current fault        | Fault0050           | Protect0140 occurs >3 times in 5 minutes. | On    | Off     | On   | On    |
| UXUU34   |          | Bit7  | Reserved                           |                     |   |       |         |      |       |
|          |          | Bit6  | DCI fault                          | Fault0070           | Protect0170 occurs >3 times in 5 minutes. | On    | Off     | On   | On    |
|          |          | Bit5  | Reserved                           |                     |   |       |         |      |       |
|          | PFault L | Bit4  | Static GFCI Fault                  | Fault0090           | Protect0610 occurs >3 times in 5 minutes. | On    | Off     | On   | On    |
|          | Prault L | Bit3  | Grid relay fault                   | Fault0100           | Protect0020 occurs >3 times in 5 minutes. | On    | Off     | On   | On    |
|          |          | Bit2  | Bus imbalance fault                | Fault0110           | Protect0070 occurs >3 times in 5 minutes. | On    | Off     | On   | On    |
|          |          | Bit1  | Bus(sum) low voltage               | Fault0120           | Protect0080 occurs >3 times in 5 minutes. | On    | Off     | On   | On    |
|          |          | Bit0  | Bus(sum) over voltage fault        | Fault0130           | Protect0090 occurs >3 times in 5 minutes. | On    | Off     | On   | On    |

#### PVI 14/20/23/28/36TL: PFault Register (0x0034)

### 4.2 Inverter Warning Registers

Inverter warnings are low severity warning indications; and the inverter can continue to produce power, possibly in derating state. The inverter can resume normal operation if the warning condition clears.

#### PVI 14/20/23/28/36TL: Warning Register (0x0035)

| Register |        |       | Alarm/Protection/                | LCD Display |   |       | LED | Reado | ut         |
|----------|--------|-------|----------------------------------|-------------|---|-------|-----|-------|------------|
| Address  | Name   | /Bits | Fault                            | Readout     | Description                                       | Power | Run | Grid  | Fault      |
|          |        | Bit15 | Reserved                         |             |   |       |     |       |            |
|          |        | Bit14 | Reserved                         |             |   |       |     |       |            |
|          |        | Bit13 | Reserved                         |             |   |       |     |       |            |
|          | Warn H | Bit12 | Reserved                         |             |   |       |     |       |            |
|          |        | Bit11 | Reserved                         |             |   |       |     |       |            |
|          |        | Bit10 | Reserved                         |             |   |       |     |       |            |
|          |        | Bit9  | LCD EEPROM fault                 | Warn0090    | Read/write to the EEPROM LCD failed.              | On    | On  | On    | Slow Blink |
|          |        | Bit8  | Reserved                         |             |   |       |     |       |            |
|          |        | Bit7  | Not used                         | Warn0070    | This warning can be ignored                       |       |     |       |            |
| 0x0035   |        | Bit6  | Reserved                         |             |   |       |     |       |            |
|          |        | Bit5  | Temperature sensor<br>fault      | Warn0050    | Temperature reading is out of range (<-25 D.C).   | On    | On  | On    | Slow Blink |
|          | Warn L | Bit4  | Not used                         | Warn0040    | This warning can be ignored                       |       |     |       |            |
|          | warn L | Bit3  | DSP EEPROM fault                 | Warn0030    | Read/write to EEPROM failed. Parameters stored in | 0.5   | 0.5 | 0     |            |
|          |        | -     |                                  |             | EEPROM are reset to default.                      | On    | On  | On    | Slow Blink |
|          |        | Bit2  | Internal communication<br>failed | CommErr     | SPI communication between DSP and LCD fails.      | On    | On  | On    | Slow Blink |
|          |        | Bit1  | Internal fan error               | IntFanErr   | Internal fan failed or fan is blocked.            | On    | On  | On    | Slow Blink |
|          |        | Bit0  | External fan error               | ExtFanErr   | External fan failed or fan is blocked.            | On    | On  | On    | Slow Blink |

# 4.3 Inverter Fault Registers

Inverter Faults are medium severity faults which will stop the inverter from running. It is possible for the inverter to restart if the fault clears.

### PVI 14/23/26/36TL: Fault0 Register (0x0036)

| Register |          |        | Alarm/Protection/                          | LCD Display  |   |       | LEI | D Read | out         |
|----------|----------|--------|--|--------------|---|-------|-----|--------|-------------|
| Address  | Name     | Bits   | Fault                                      | Readout      | Description   | Power | Run | Grid   | Fault       |
|          |          | BIT 15 | Sampling offset of output<br>current error | Protect0010  | The sampling offset of output current is out of range.<br>(>3%)                           | On    | Off | On     | Quick Blink |
|          |          | Bit14  | Over-temperature protection                | TempOver     | Internal over temperature is detected. (Power Module >107°C, Inner cabinet > 70°C)        | On    | Off | On     | Quick Blink |
|          | Fault OH | Bit13  | Grid relay error                           | Protect0020  | The voltages at both sides of grid relay do not match each other.                         | On    | Off | On     | Quick Blink |
|          |          | Bit12  | Loss of main                               | GridV.OutLim | One phase of grid has been lost.  | On    | Off | Blink  | Quick Blink |
|          |          | Bit11  | Grid under frequency                       | GridF.OutLim | Frequency is low. (<59.3Hz)   | On    | Off | Blink  | Quick Blink |
|          |          | Bit10  | Grid Over frequency                        | GridF.OutLim | Frequency is high. (>60.5Hz)  | On    | Off | Blink  | Quick Blink |
| 0x0036   |          | Bit9   | Inverter over current                      | Protect0030  | Inverter over current detected. (>65A)  | On    | Off | On     | Quick Blink |
|          |          | Bit8   | Grid phase voltage out of range            | GridV.OutLim | One phase of the voltage is out of range. (<88%> 110%, trip point is settable)            | On    | Off | Blink  | Quick Blink |
|          |          | Bit7   | Grid line voltage out of<br>range          | GridV.OutLim | One of the line voltages is out range. (<88%,> 110%, trip point is settable)              | On    | Off | Blink  | Quick Blink |
|          |          | Bit6   | PV1 over current                           | Protect0040  | One boost of MPPT1 is over current. (>25A)  | On    | Off | On     | Quick Blink |
|          |          | Bit5   | Reserved                                   |              |   |       |     |        |             |
|          | Fault OL | Bit4   | Inverter soft start timeout                | Protect0050  | Inverter soft start failure. (previous > 10s)   | On    | Off | On     | Quick Blink |
|          | Fault OL | Bit3   | Bus soft start timeout                     | Protect0060  | The soft start of boost fails. BUS could not reach the reference voltage. (previous >10s) | On    | Off | On     | Quick Blink |
|          |          | Bit2   | Bus imbalance                              | Protect0070  | BUS imbalance is detected. (+/-BUS imbalance >100V)                                       | On    | Off | On     | Quick Blink |
|          |          | Bit1   | Bus(sum) low voltage                       | Protect0080  | BUS voltage is low in operation (sum<100V).   | On    | Off | On     | Quick Blink |
|          |          | BITU   | Bus(sum) over voltage<br>(firmware)        | Protect0090  | BUS over voltage (Sum of +/-BUS >925V) is detected by firmware.                           | On    | Off | On     | Quick Blink |

#### PVI 20TL: Fault0 Register (0x0036)

| Register |          |         | Alarm/Protection/                          | LCD Display  |   |       | LEC | ) Read | out         |
|----------|----------|---------|--|--------------|---|-------|-----|--------|-------------|
| Address  |          | /Bits   | Fault                                      | Readout      | Description   | Power | Run | Grid   | Fault       |
|          |          | IBIT 15 | Sampling offset of output<br>current error | IntProtectA  | The sampling offset of output current is out of range.<br>(>3%)                           | On    | Off | On     | Quick Blink |
|          |          | Bit14   | Over-temperature protection                | TempOver     | Internal over temperature is detected. (Power Module >107°C, Inner cabinet > 68°C)        | On    | Off | On     | Quick Blink |
|          | Fault OH | Bit13   | Grid relay error                           | IntProtectB  | The voltages at both sides of grid relay do not match each other.                         | On    | Off | On     | Quick Blink |
|          |          | Bit12   | Loss of main                               | GridV.OutLim | One phase of grid has been lost.  | On    | Off | Blink  | Quick Blink |
|          |          | Bit11   | Grid under frequency                       | GridF.OutLim | Frequency is low. (<59.3Hz)   | On    | Off | Blink  | Quick Blink |
|          |          | Bit10   | Grid Over frequency                        | GridF.OutLim | Frequency is high. (>60.5Hz)  | On    | Off | Blink  | Quick Blink |
|          |          | Bit9    | Inverter over current                      | IntProtectC  | Inverter over current detected. (>58A)  | On    | Off | On     | Quick Blink |
| 0x0036   |          | Bit8    | Grid phase voltage out of range            | GridV.OutLim | One phase of the voltage is out of range. (<88%> 110%, trip point is settable)            | On    | Off | Blink  | Quick Blink |
|          |          | Bit7    | Grid line voltage out of<br>range          | GridV.OutLim | One of the line voltages is out range. (<88%,> 110%, trip point is settable)              | On    | Off | Blink  | Quick Blink |
|          |          | Bit6    | PV1 over current                           | IntProtectD  | Boost Module 1 (MPPT1) over current detected. (>45A)                                      | On    | Off | On     | Quick Blink |
|          |          | Bit5    |  |              |   |       |     |        |             |
|          | Fault OL | Bit4    | Inverter soft start timeout                | IntProtectE  | Inverter soft start failure. (previous > 10s)   | On    | Off | On     | Quick Blink |
| F        |          | Bit3    | Bus soft start timeout                     | IntProtectF  | The soft start of boost fails. BUS could not reach the reference voltage. (previous >10s) | On    | Off | On     | Quick Blink |
|          |          | Bit2    | Bus imbalance                              | IntProtectG  | BUS imbalance is detected. (+/-BUS imbalance >130V)                                       | On    | Off | On     | Quick Blink |
|          |          | Bit1    | Bus(sum) low voltage                       | IntProtectH  | BUS voltage is low in operation (sum<260V).   | On    | Off | On     | Quick Blink |
|          |          | BitO    | Bus(sum) over voltage<br>(firmware)        | IntProtectI  | BUS over voltage (Sum of +/-BUS >840V) is detected by firmware.                           | On    | Off | On     | Quick Blink |

# PVI 14/23/28/36TL: Fault 1 Register (0x0037)

| Register |          |       | Alarm/Protection/               | LCD Display  |  | LED Readout |     |      |             |  |  |
|----------|----------|-------|---------------------------------|--------------|--|-------------|-----|------|-------------|--|--|
| Address  |          | /Bits | Fault                           | Readout      | Description  | Power       | Run | Grid | Fault       |  |  |
|          |          | Bit15 | Leakage current sensor<br>error | Protect0100  | Leakage current sensor failure.  | On          | Off | On   | Quick Blink |  |  |
|          |          | Bit14 | Reserved                        |              |  |             |     |      |             |  |  |
|          |          | Bit13 | Reserved                        |              |  |             |     |      |             |  |  |
|          | Fault 1H | Bit12 | Power module protection         | Protect0120  | Power module tripped.  | On          | Off | On   | Quick Blink |  |  |
|          |          | Bit11 | Inverter current imbalance      | Protect0130  | Inverter current imbalance detected. (>8%)   | On          | Off | On   | Quick Blink |  |  |
|          |          | Bit10 | Reserved                        |              |  |             |     |      |             |  |  |
|          |          | Bit9  | Grid voltage imbalance          | GridV.OutLim | Imbalance for grid voltage detected. (>2.6%)   | On          | Off | On   | Quick Blink |  |  |
|          |          | Bit8  | Inverter hardware over current  | Protect0140  | Inverter over current detected.  | On          | Off | On   | Quick Blink |  |  |
| 0x0037   |          | Bit7  | MCU protection                  | Protect0150  | Grid errors detected by assistant MiniMCU. (Grid error)  | On          | Off | On   | Quick Blink |  |  |
|          |          | Bit6  | Reserved                        |              |  |             |     |      |             |  |  |
|          |          | Bit5  | Frequency detection fault       | Protect0160  | Inverter cannot identify frequency: 50/60Hz.<br>(<55Hz, >70Hz)                                     | On          | Off | On   | Quick Blink |  |  |
|          | Fault 1L | Bit4  | Dynamic leakage current<br>high | GFCIErr      | Leakage current fault. (Sudden > 30mA)   | On          | Off | On   | Quick Blink |  |  |
|          |          | Bit3  | Insulation resistance low       | IsolationErr | Insulation resistance is low.<br>(<250kOhm for 23/28TL) (<220kOhm for 20TL)<br>(<220kohm for 14TL) | On          | Off | On   | Quick Blink |  |  |
|          |          | Bit2  | DCI high                        | Protect0170  | DC current injection failure. (>0.5%In, settable)  | On          | Off | On   | Quick Blink |  |  |
|          |          | Bit1  | DCI offset error                | Protect0180  | DCI sampling offset of current is out of range. (>10%)   | On          | Off | On   | Quick Blink |  |  |
|          |          | Bit0  | Inverter voltage offset error   | Protect0190  | Inverter sampling voltage offset is out of range. (>5%)  | On          | Off | On   | Quick Blink |  |  |

# PVI 20TL: Fault 1 Register (0x0037)

| Register |          |       | Alarm/Protection/               | LCD Display  |  |       | LED F | Readou | ıt          |
|----------|----------|-------|---------------------------------|--------------|--|-------|-------|--------|-------------|
| Address  | Name/    | Bits  | Fault                           | Readout      | Description  | Power | Run   | Grid   | Fault       |
|          |          | Bit15 | Leakage current sensor<br>error | IntProtectJ  | Leakage current sensor failure.                                | On    | Off   | On     | Quick Blink |
|          |          | Bit14 | Bus overvoltage (hardware)      | IntProtectK  | BUS over voltage detected by hardware.                         | On    | Off   | On     | Quick Blink |
|          |          | Bit13 | Reserved                        |              |  |       |       |        |             |
|          | Fault 1H | Bit12 | Power module protection         | IntProtectL  | Power module tripped.  | On    | Off   | On     | Quick Blink |
|          |          | Bit11 | Inverter current imbalance      | IntProtectM  | Inverter current imbalance detected. (>8%)                     | On    | Off   | On     | Quick Blink |
|          |          | Bit10 | Reserved                        |              |  |       |       |        |             |
|          |          | Bit9  | Grid voltage imbalance          | GridV.OutLim | Imbalance for grid voltage detected. (>2.6%)                   | On    | Off   | On     | Quick Blink |
|          |          | Bit8  | Inverter hardware over current  | IntProtectN  | Inverter over current detected.                                | On    | Off   | On     | Quick Blink |
| 0x0037   |          | Bit7  | MCU protection                  | Interotecto  | Grid errors detected by assistant MiniMCU. (Grid error)        | On    | Off   | On     | Quick Blink |
|          |          | Bit6  | Reserved                        |              |  |       |       |        |             |
|          |          | Bit5  | Frequency detection fault       | IntProtectP  | Inverter cannot identify frequency: 50/60Hz.<br>(<55Hz, >70Hz) | On    | Off   | On     | Quick Blink |
|          | Fault 1L | Bit4  | Dynamic leakage current<br>high | GFCIErr      | Leakage current fault. (Sudden > 30mA)                         | On    | Off   | On     | Quick Blink |
|          |          | Bit3  | Insulation resistance low       | IsolationErr | Insulation resistance is low. (<600kohm)                       | On    | Off   | On     | Quick Blink |
|          |          | Bit2  | DCI high                        | IntProtectQ  | DC current injection failure. (120mA, settable)                | On    | Off   | On     | Quick Blink |
|          |          | Bit1  | DCI offset error                | IntProtectR  | DCI sampling offset of current is out of range. (>10%)         | On    | Off   | On     | Quick Blink |
|          |          | Bit0  | Inverter voltage offset error   | IntProtectS  | Inverter sampling voltage offset is out of range. (>5%)        | On    | Off   | On     | Quick Blink |

# PVI 23/28/36TL: Fault 2 Register (0x0038)

| Register |          |       | Alarm/Protection/                     | LCD Display |  |       | LED I | Reado | ut          |
|----------|----------|-------|---------------------------------------|-------------|--|-------|-------|-------|-------------|
| Address  | Name     | Bits  | Fault                                 | Readout     | Description  | Power | Run   | Grid  | Fault       |
|          |          | Bit15 | Reserved                              |             |  |       |       |       |             |
|          |          | Bit14 | Reserved                              |             |  |       |       |       |             |
|          |          | Bit13 | Reserved                              |             |  |       |       |       |             |
|          | Fault 2H | Bi12  | Reserved                              |             |  |       |       |       |             |
|          | rault ZH | Bit11 | Reserved                              |             |  |       |       |       |             |
|          |          | Bit10 | PV1 over voltage                      | PV1VoltOver | MPPT1 over-voltage (>1020V)  | On    | Off   | On    | Quick Blink |
|          |          | Bit9  | PV1 input reverse<br>connection       | PV1Reverse  | Negative input current of MPPT1 is detected. (<-2A)                          | On    | Off   | On    | Quick Blink |
| 0x0038   |          | Bit8  | Reserved                              |             |  |       |       |       |             |
|          |          | Bit7  | Inverter open-loop<br>self-test error | Protect0230 | The voltage difference between inverter voltage and command voltage >50V.    | On    | Off   | On    | Quick Blink |
| 0,0038   |          | Bit6  | PV abnormal input                     | Protect0260 | Input Mode detection failure. MPPT1/MPP2 Difference > 20V for Parallel Mode. |       | Off   | On    | Quick Blink |
|          |          | Bit5  | PV2 over voltage                      | PV2VoltOver | MPPT2 over-voltage (>1020V)  | On    | Off   | On    | Quick Blink |
|          | Fault 2L | Bit4  | PV2 over current                      | Protect0240 | One boost of MPPT2 over current (>25A)                                       | On    | Off   | On    | Quick Blink |
|          | Fduit 2L | Bit3  | PV2 input reverse<br>connection       | PV2Reverse  | Negative input current of MPP2 detected. (<-2A)                              | On    | Off   | On    | Quick Blink |
|          |          | Bit2  | Input and output<br>power mismatch    | Protect0220 | The difference between input and output power is out of range. (>50%)        | On    | Off   | On    | Quick Blink |
|          |          | Bit1  | Internal hardware<br>error            | Protect0210 | Inverter cannot export any running current. (<1A but required >25A)          | On    | Off   | On    | Quick Blink |
|          |          | Bit0  | Reserved                              |             |  |       |       |       |             |

# PVI 20TL: Fault 2 Register (0x0038)

| Register | -        |       | Alarm/Protection/                     | LCD Display |   | LED Readout |     |      |             |
|----------|----------|-------|---------------------------------------|-------------|---|-------------|-----|------|-------------|
| Address  |          |       | Fault                                 | Readout     | Description   | Power       | Run | Grid | Fault       |
|          | Fault 2H | Bit15 | Arc Protection                        | Arc Protect | ARC detection failure<br>ARC Fault is a "permanent" fault. Site visit is required to clear<br>it by turning DC and AC Power On and Off. | On          | Off | On   | Quick Blink |
|          |          | Bit14 | Static GFI Protect                    | IntProtectY | Continuous leakage current fault. (Continuous > 300mA)  | On          | Off | On   | Quick Blink |
|          |          | Bit13 | Arcboard abnormal                     | ArcboardErr | ARC board self-test failure   | On          | Off | On   | Quick Blink |
|          |          | Bi12  | Reserved                              |             |   |             |     |      |             |
|          |          | Bit11 | Reserved                              |             |   |             |     |      |             |
|          |          | Bit10 | PV1 over voltage                      | PV1VoltOver | MPPT1 over-voltage (>610V)  | On          | Off | On   | Quick Blink |
|          |          | Bit9  | PV1 input reverse<br>connection       | PV1Reverse  | Negative input current of MPPT1 is detected. (<-2A)   | On          | Off | On   | Quick Blink |
|          |          | Bit8  | Reserved                              |             |   |             |     |      |             |
| 0x0038   | Fault 2L | Bit7  | Inverter open-loop<br>self-test error | IntProtectW | The voltage difference between inverter voltage and command voltage >50V.   | On          | Off | On   | Quick Blink |
|          |          | Bit6  | PV abnormal input                     | IntProtectZ | Input Mode detection failure. MPPT1/MPP2 Difference > 20V for Parallel Mode.  |             | Off | On   | Quick Blink |
|          |          | Bit5  | PV2 over voltage                      | PV2VoltOver | MPPT2 over-voltage (>610V)  | On          | Off | On   | Quick Blink |
|          |          | Bit4  | PV2 over current                      | IntProtectX | MPPT2 over current (>45A)   | On          | Off | On   | Quick Blink |
|          |          | Bit3  | PV2 input reverse<br>connection       | PV2Reverse  | Negative input current of MPP2 detected. (<-2A)   | On          | Off | On   | Quick Blink |
|          |          | Bit2  | Input and output<br>power mismatch    | IntProtectV | The difference between input and output power is out of range. (>50%)   | On          | Off | On   | Quick Blink |
|          |          | Bit1  | Internal hardware<br>error            | IntProtectU | Inverter cannot export any running current. (<1A but required >35A)   | On          | Off | On   | Quick Blink |
|          |          | Bit0  | Reserved                              |             |   |             |     |      |             |

# PVI 14TL: Fault 2 Register (0x0038)

| Register |          |       | Alarm/Protection/                     | LCD Display |   | LED Readout |     |      |             |
|----------|----------|-------|---------------------------------------|-------------|---|-------------|-----|------|-------------|
| Address  |          |       | Fault                                 | Readout     | Description   | Power       | Run | Grid | Fault       |
|          | Fault 2H | Bit15 | Arc Protection                        | Arc Protect | ARC detection failure<br>ARC Fault is a "permanent" fault. Site visit is required to clear<br>it by turning DC and AC Power On and Off. | On          | Off | On   | Quick Blink |
|          |          | Bit14 | Static GFI Protect                    | IntProtectY | Continuous leakage current fault. (Continuous > 300mA)  | On          | Off | On   | Quick Blink |
|          |          | Bit13 | Arcboard abnormal                     | ArcboardErr | ARC board self-test failure   | On          | Off | On   | Quick Blink |
|          |          | Bi12  | Reserved                              |             |   |             |     |      |             |
|          |          | Bit11 | Reserved                              |             |   |             |     |      |             |
|          |          | Bit10 | PV1 over voltage                      | PV1VoltOver | MPPT1 over-voltage (>610V)  | On          | Off | On   | Quick Blink |
|          |          | Bit9  | PV1 input reverse<br>connection       | PV1Reverse  | Negative input current of MPPT1 is detected. (<-2A)   | On          | Off | On   | Quick Blink |
|          |          | Bit8  | Reserved                              |             |   |             |     |      |             |
| 0x0038   | Fault 2L | Bit7  | Inverter open-loop<br>self-test error | Protect0230 | The voltage difference between inverter voltage and<br>command voltage >50V.  | On          | Off | On   | Quick Blink |
|          |          | Bit6  | PV abnormal input                     | Protect0260 | Input Mode detection failure. MPPT1/MPP2 Difference > 20V for Parallel Mode.  |             | Off | On   | Quick Blink |
|          |          | Bit5  | PV2 over voltage                      | PV2VoltOver | MPPT2 over-voltage (>610V)  | On          | Off | On   | Quick Blink |
|          |          | Bit4  | PV2 over current                      | Protect0240 | One boost of MPPT2 over current (>45A)  | On          | Off | On   | Quick Blink |
|          |          | Bit3  | PV2 input reverse<br>connection       | PV2Reverse  | Negative input current of MPP2 detected. (<-2A)   | On          | Off | On   | Quick Blink |
|          |          | Bit2  | Input and output<br>power mismatch    | Protect0220 | The difference between input and output power is out of range. (>50%)   | On          | Off | On   | Quick Blink |
|          |          | Bit1  | Internal hardware<br>error            | Protect0210 | Inverter cannot export any running current. (<1A but required >25A)   | On          | Off | On   | Quick Blink |
|          |          | Bit0  | Reserved                              |             |   |             |     |      |             |

# PVI 23/28/36TL: Fault 3 Register (0x0039)

ARC Fault is a "permanent" fault. Site visit is required to clear it by turning DC and AC Power On and Off.

| Register | er       |       | Alarm/Protection/ | LCD Display |                      | LED Readout |     |      |             |  |
|----------|----------|-------|-------------------|-------------|----------------------|-------------|-----|------|-------------|--|
| Address  | Name/    | 'Bits | Fault             | Readout     | Description          | Power       | Run | Grid | Fault       |  |
|          | Fault 3H | Bit15 | ARC protection    | ARC Protect | ARC failure detected | On          | Off | On   | Quick Blink |  |
|          |          | Bit14 | Reserved          |             |                      |             |     |      |             |  |
|          |          | Bit13 | Reserved          |             |                      |             |     |      |             |  |
|          |          | Bi12  | Reserved          |             |                      |             |     |      |             |  |
|          |          | Bit11 | Reserved          |             |                      |             |     |      |             |  |
|          |          | Bit10 | Reserved          |             |                      |             |     |      |             |  |
|          |          | Bit9  | Reserved          |             |                      |             |     |      |             |  |
| 00020    |          | Bit8  | Reserved          |             |                      |             |     |      |             |  |
| 0x0039   |          | Bit7  | Reserved          |             |                      |             |     |      |             |  |
|          |          | Bit6  | Reserved          |             |                      |             |     |      |             |  |
|          |          |       | Reserved          |             |                      |             |     |      |             |  |
|          |          | Bit4  | Reserved          |             |                      |             |     |      |             |  |
|          |          | Bit3  | Reserved          |             |                      |             |     |      |             |  |
|          |          | Bit2  | Reserved          |             |                      |             |     |      |             |  |
|          |          | Bit1  | Reserved          |             |                      |             |     |      |             |  |
|          |          | Bit0  | Reserved          |             |                      |             |     |      |             |  |

# PVI 23/28/36TL: Fault 4 Register (0x003A)

| Register | ister    |       | Alarm/Protection/                | LCD Display  | y l  |       | LED Readout |      |             |  |  |
|----------|----------|-------|----------------------------------|--------------|--|-------|-------------|------|-------------|--|--|
| Address  | Name/    | 'Bits | Fault                            | Readout      | Description  | Power | Run         | Grid | Fault       |  |  |
|          |          | Bit15 | Reserved                         |              |  |       |             |      |             |  |  |
|          |          | Bit14 | Reserved                         |              |  |       |             |      |             |  |  |
|          |          | Bit13 | Reserved                         |              |  |       |             |      |             |  |  |
|          |          | Bi12  | Reserved                         |              |  |       |             |      |             |  |  |
|          |          | Bit11 | Reserved                         |              |  |       |             |      |             |  |  |
|          |          | Bit10 | Reserved                         |              |  |       |             |      |             |  |  |
|          |          | Bit9  | Reserved                         |              |  |       |             |      |             |  |  |
|          |          | Bit8  | Reserved                         |              |  |       |             |      |             |  |  |
| 0x003A   | Fault 4L | Bit7  | Reserved                         |              |  |       |             |      |             |  |  |
| 0,0034   |          | Bit6  | Reserved                         |              |  |       |             |      |             |  |  |
|          |          | Bit5  | Reserved                         |              |  |       |             |      |             |  |  |
|          |          | Bit4  | Reserved                         |              |  |       |             |      |             |  |  |
|          |          | Rit3  | PV Module<br>Configuration Error | Protect0590  | DC/AC ratio is too large.                              | On    | Off         | On   | Quick Blink |  |  |
|          |          | Bit2  | Arc board failure                | Arcboard Err | ARC board self-test fails                              | On    | Off         | On   | Quick Blink |  |  |
|          |          | Bit1  | Static GFCI high                 | Protect0610  | Continuous leakage current fault. (Continuous > 300mA) | On    | Off         | On   | Quick Blink |  |  |
|          |          | Bit0  | 15V of control board<br>low      | Protect0620  | The internal 15V power voltage is low                  | On    | Off         | On   | Quick Blink |  |  |